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Executive Biography

Boris Balacheff

HP Fellow & VP
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Boris Balacheff is Chief Technologist for System Security Research and Innovation at HP Labs where he leads security research strategy. An established technologist and thought leader in platform and infrastructure security, he also works closely with HP business units, customers, and partners on cybersecurity futures and the evolution of the threat landscape.

Boris is known and respected for his expertise and strategic insights both inside and outside HP. Today he is driving a HP Labs security research agenda ranging personal device architectures to emerging cyber-physical solutions. He is deeply engaged in shaping technology strategy directions across the HP portfolio, and drives industry standards participation and academic collaborations. Boris represents HP on the Board of Directors of the Trusted Computing Group (TCG) and chairs its Certification Program Committee.

Throughout his security research career at HP Labs, Boris has consistently championed cybersecurity innovation, both internally and externally. He successively led smartcard research, HP's Trusted Computing research agenda, or cloud-client security research in support of increasingly cloud-based and highly mobile customer IT needs.

Boris is one of the early inventors of Trusted Computing technology, and co-authored the first technical book on this topic. Early in his career, Boris represented HP on the Technical Committee of the smartcard-focused PC/SC working group. He later served on the Technical Committees of both the Trusted Computing Platform Alliance (TCPA) and the subsequent Trusted Computing Group, where he co-authored the first TCG specifications.

A named inventor on over 40 US patents, Boris Balacheff joined HP Labs with a French Diplôme d'Ingénieur degree from ISIMA, a French engineering school in Applied Mathematics and Computer Science.