



Dr. Partha Ranganathan

Corporate Fellow, HP Labs
Hewlett-Packard Company

Dr. Partha Ranganathan is a corporate fellow at HP Labs, HP's central research arm, where he currently leads a research program on future data-centric data centers. His research interests are in systems architecture and manageability, energy-efficiency, and systems modeling and evaluation. He has done extensive work in these areas, including key contributions around energy-aware user interfaces, heterogeneous multicore processors, power capping and power-aware server designs, federated enterprise power management, energy modeling and benchmarking, disaggregated blade server architectures and, most recently, storage hierarchy and systems redesign for non-volatile memory.

Ranganathan's work has had broad impact on industry and academia and has been featured in various venues, including *The New York Times*, *Wall Street Journal*, *Business Week*, *San Francisco Chronicle*, *Times of India*, *Slashdot*, *YouTube* and *Tom's Hardware Guide*. He has been named one of the world's top young innovators by MIT Technology Review and has been recognized with several other awards, including Rice University's Outstanding Young Engineering Alumni award. Ranganathan received his Bachelor of Technology degree from the Indian Institute of Technology, Madras and Master of Science and Doctorate of Philosophy degree from Rice University in Houston.