



Chandrakant Patel

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Chandrakant Patel is a fellow at HP Labs, the company's central research facility, where he is responsible for strategically engaging in thermo-mechanical research for future microprocessors, workstations, servers and data centers.

Patel founded HP Labs' thermal technology research program in the early 1990s, and subsequently the data-center architecture program. He pioneered a holistic approach to power and cooling that encompasses everything from chips to systems to racks to the data center itself. With partners in HP's product research and development groups, he started a virtual thermal community known as the HP Cool Team.

Patel's work has been incorporated into HP products and services, including its Adaptive Infrastructure offerings, and also used to manage HP's own information technology infrastructure.

Patel joined HP Labs in 1991, initially leading the cooling and packaging research of the Wide Word microprocessor. This research contributed to what later became Intel's Itanium microprocessor.

Patel has taught computer-aided design as an adjunct faculty member at Chabot College in Hayward, Calif., since 1990, and graduate-level thermal management courses at University of California, Berkeley Extension and Santa Clara University since 1999.

He has authored many refereed journal and conference papers in the area of electronics cooling and has been granted 68 U.S. patents. He is a senior member of IEEE, a member of the Engineering Advisory Board at Chabot College and a member of the Industrial Advisory Board at Santa Clara University.

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